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| Notice of References Cited | Application/Control No. 09/905,398 | Applicant(s)/Patent Under Reexamination LAYADI ET AL. | |
| | Examiner Anh D. Mai | Art Unit 2814 | Page 1 of 1 |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|------|----------------|
| | A | US- | | | |
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FOREIGN PATENT DOCUMENTS

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| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|---|
| | U | S. Wolf et al. Silicon Processing for the VLSI Era. 1986, Vol. 1, pp. 407-409. |
| | V | |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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